Docket No.: SMQ-067/P5716

App No.: 10/016196 Docket No.: SMQ-06 Inventor: William B. GIST et al.
Title: SINGLE-ENDED IO WITH DYNAMIC SYNCHRONOUS

**DESKEWING ARCHITECTURE** REPLACEMENT SHEET

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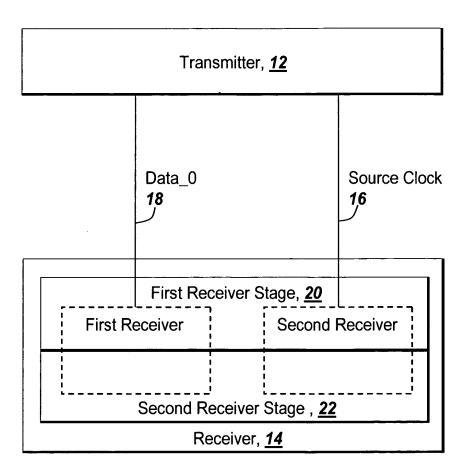


Fig. 1

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Second Receiver Stage Deskewing Circuit 52 Phase Locked Loop Circuit/Delay-Locked Loop Circuit 30 44 Delay Circuit Controlled Delay Line  $\subseteq$ Frequency Multiplier Voltage Controlled Oscillator/Voltage Phase Detector Delay Elements 34B Delay Elements FSM 23B **23A** First Receiver Stage Fig. 2 رھ

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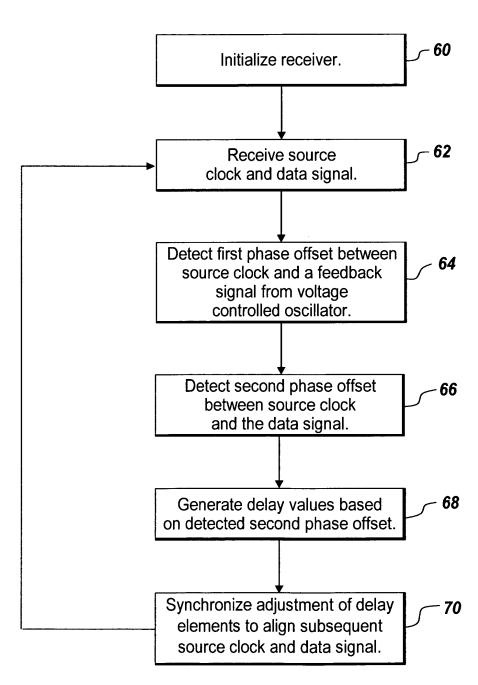


Fig. 3